

**REMARKS/ARGUMENT**

The Examiner telephoned the undersigned on April 11, 2002 requesting that claim 3, which was amended in Appendix B of the Amendment dated March 4, 2002, be placed in Appendix A. Claim 3 has now been properly amended and appears in both Appendix A and Appendix B.

New claims 12-14 have also been added and are directed to additional disclosed aspects of the invention.

The specification has been amended to correct a typographical error contained therein.

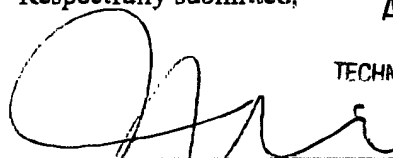
In view of the foregoing, allowance of claims 1-14 is requested.

I hereby certify that this correspondence is being transmitted via facsimile to the Assistant Commissioner for Patents at facsimile No. 703-308-7724, on April 26, 2002:

\_\_\_\_\_  
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Name of applicant, assignee or  
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April 26, 2002  
\_\_\_\_\_  
Date of Signature

JAF:lac

Respectfully submitted,



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## APPENDIX A

"CLEAN" VERSION OF EACH PARAGRAPH/SECTION/CLAIM  
37 C.F.R. § 1.121(b)(ii) AND (c)(i)

SPECIFICATION:

Replacement for the paragraph beginning at page 9, line 13:

21  
1  
In embodiments of the present invention based on the circuits shown in Fig. 25 and Fig. 26, the impedance element 18 having a frequency characteristic is connected between the emitter of the transistor Tr1 constituting the amplifying circuit 14 and ground, as shown in Figs. 7 and 8. As shown in Figs. 9 and 10, respectively, the impedance element 18 may be a series circuit which is obtained by connecting in series a parallel circuit of a DC bias resistor R20 and a capacitor 20, and a parallel resonance circuit of an inductor L20 and a capacitor 21. As shown Figs. 11 and 12, respectively, a series circuit of a capacitor 22 and an inductor 21 may be further added, and the series circuit is connected to the emitter of the transistor Tr1 in the circuit shown in Figs. 9 and 10.

CLAIMS (with indication of amended or new):

3  
1  
AMENDED 3. An oscillator according to claim 1, wherein said element comprises a dielectric or piezoelectric material.

3  
1  
NEW 12. An oscillator according to claim 1, wherein said amplifying circuit comprises an amplifier and said element is connected to one of an input terminal, an output terminal, a power supply terminal and a ground terminal of said amplifier.

NEW 13. An oscillator according to claim 4, wherein said amplifying circuit comprises an amplifier and said element is connected to one of an input terminal, an output terminal, a power supply terminal and a ground terminal of said amplifier.

NEW 14. An oscillator according to claim 6, wherein said amplifying circuit comprises an amplifier and said element is connected to one of an input terminal, an output terminal, a power supply terminal and a ground terminal of said amplifier.

**APPENDIX B**  
**VERSION WITH MARKINGS TO SHOW CHANGES MADE**  
**37 C.F.R. § 1.121(b)(iii) AND (c)(ii)**

**SPECIFICATION:**

**Paragraph at page 9, lines 13-22:**

In embodiments of the present invention based on the circuits shown in Fig. [26] 25 and Fig. [27] 26, the impedance element 18 having a frequency characteristic is connected between the emitter of the transistor Tr1 constituting the amplifying circuit 14 and ground, as shown in Figs. 7 and 8. As shown in Figs. 9 and 10, respectively, the impedance element 18 may be a series circuit which is obtained by connecting in series a parallel circuit of a DC bias resistor R20 and a capacitor 20, and a parallel resonance circuit of an inductor L20 and a capacitor 21. As shown Figs. 11 and 12, respectively, a series circuit of a capacitor 22 and an inductor 21 may be further added, and the series circuit is connected to the emitter of the transistor Tr1 in the circuit shown in Figs. 9 and 10.

**CLAIMS:**

**AMENDED** 3. An oscillator according to claim 1, wherein said element comprises a dielectric or piezoelectric material [and has a frequency characteristic].